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23494 7590 07/27/2005

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Robert E. Barnum (Depositor's name)
Robert E. Barnum (Signature)
August 9, 2005 (Date)

08/10/2005 TBESHAH2 00000032 200668 09713446

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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/713,446	11/15/2000	VENKAT V EASWAR	TI-22423	3256

TITLE OF INVENTION: MULTI-LEVEL DITHER SCREENING ON A SPLIT ARITHMETIC LOGIC UNIT PROCESSOR

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$0	\$1400	10/27/2005

EXAMINER	ART UNIT	CLASS-SUBCLASS
POON, KING Y	2624	358-003130

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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1 Robert D. Marshall, Jr.

2 W. James Brady, III

3 Frederick J. Telecky, Jr.

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

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(A) NAME OF ASSIGNEE

Texas Instruments Incorporated

(B) RESIDENCE: (CITY AND STATE OR COUNTRY)

Dallas, TX USA

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☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Authorized Signature

Robert D. Marshall, Jr.

Date August 9, 2005

Typed or printed name

Robert D. Marsahll, Jr.

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

TI-22423

Venkat V. Easwar

Art Unit: 2624

Serial No.: 09/713,446

Examiner: King Y. Poon

Filed: November 15, 2000

Conf. No.: 3256

For: Multi-Level Dither Screening on a Split Arithmetic Logic Unit Processor

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office at 703-746-4000 on the date shown below:


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NAME OF INVENTOR(S): Venkat V. Easwar	
RECEIPT DATE & SERIAL NO.: Serial No.: 09/713,446 Filing Date: November 15, 2000	
TITLE OF INVENTION: Multi-Level Dither Screening on a Split Arithmetic Logic Unit Processor	
TI FILE NO.: TI-22423	DEPOSIT ACCT. NO.: 20-0668
FAXED: 8/9/05 DUE: 10/27/05 ATTY/SEC'Y: RDM/reb	

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